

10

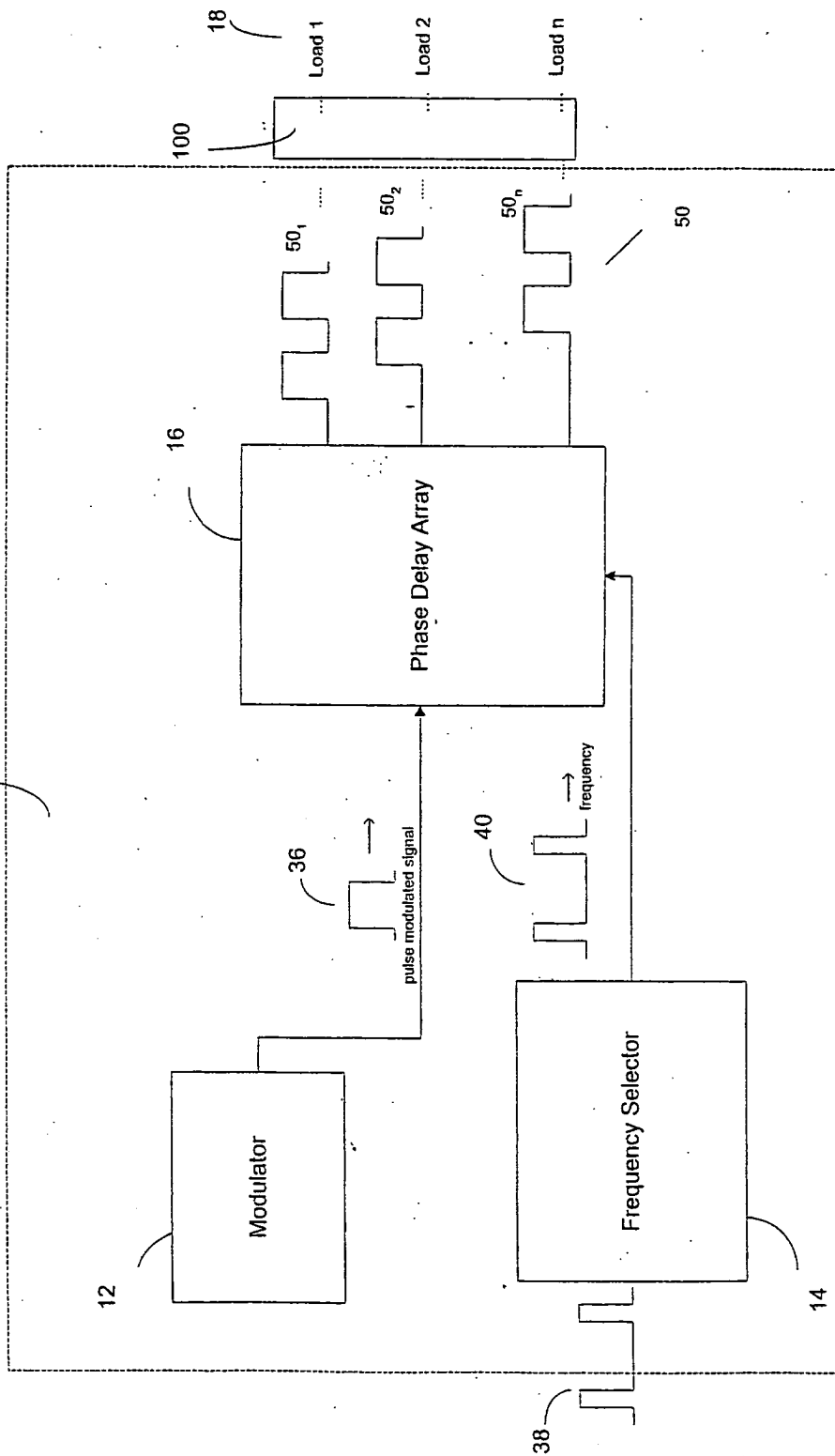


FIGURE 1

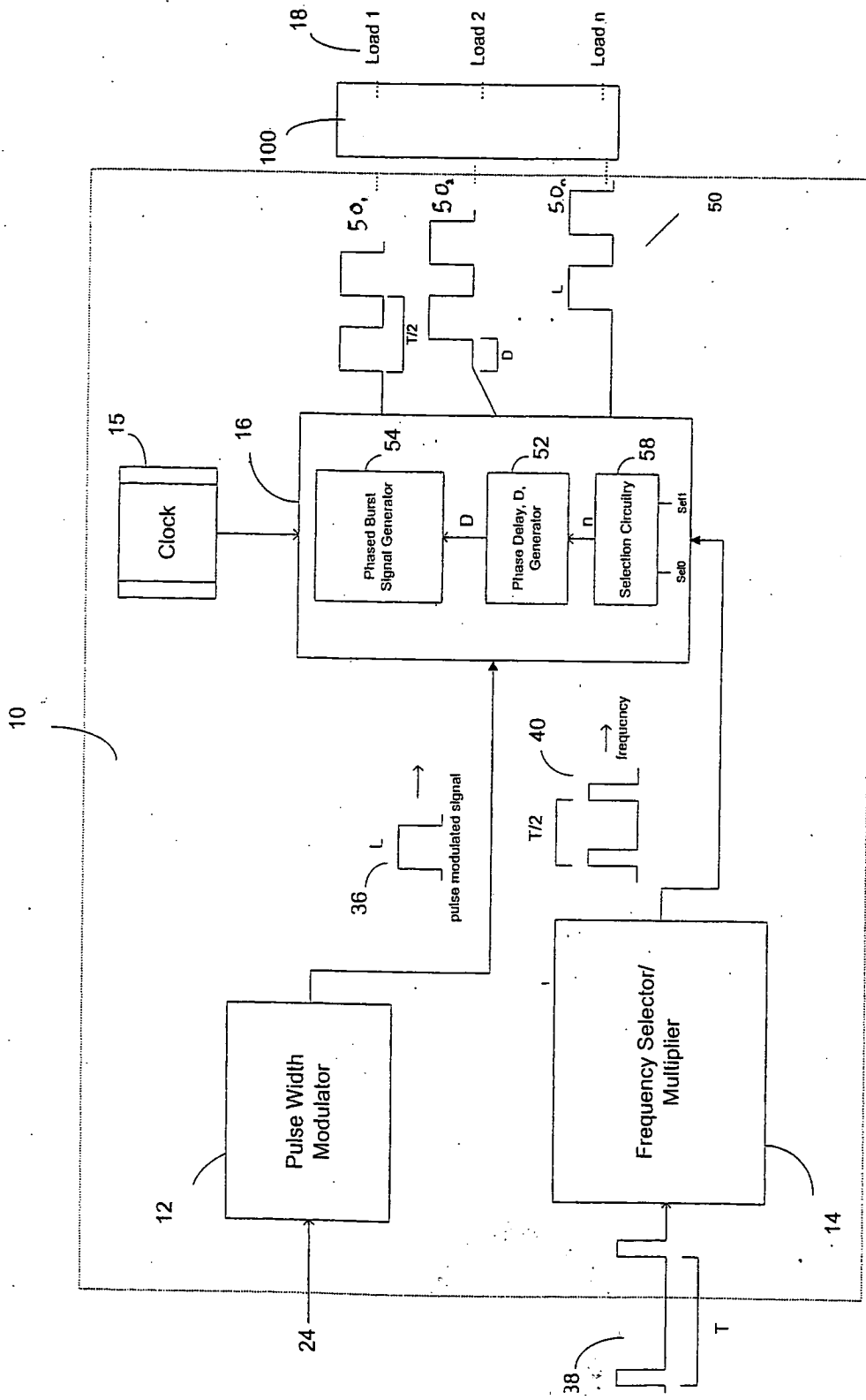


FIGURE 2

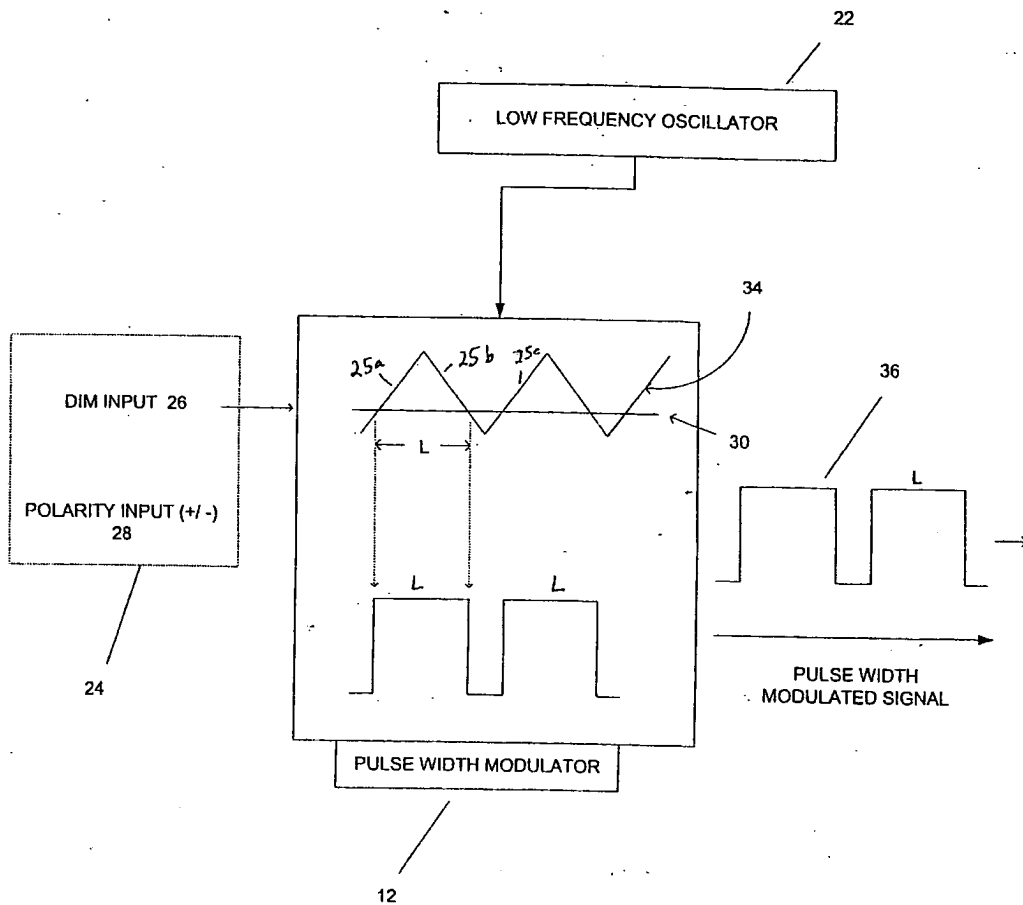


FIGURE 3

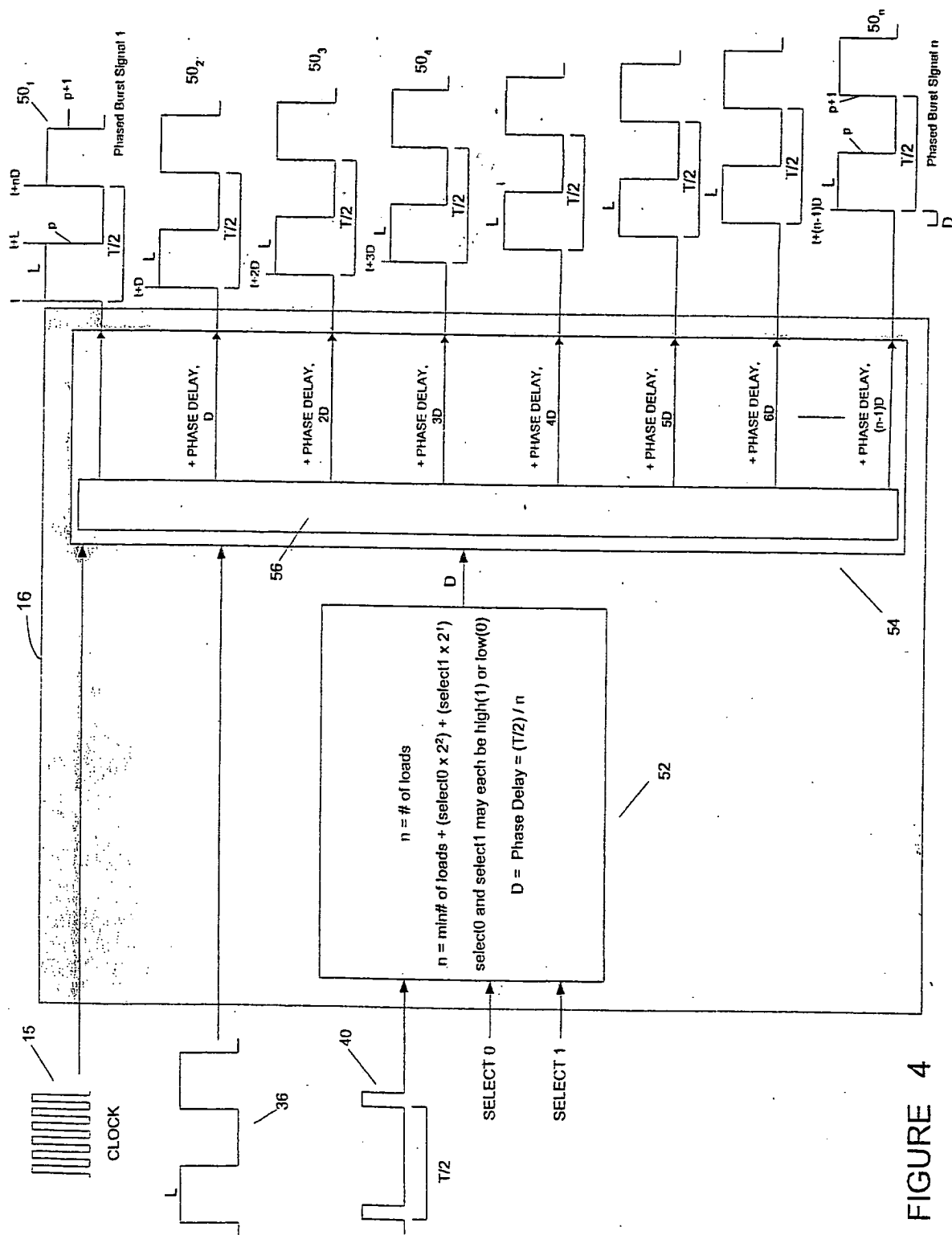


FIGURE 4

Sel0 (Select 0)	0	0	1	1
Sel1 (Select 1)	0	1	0	1
n (# of CCFLs)	6 (or min #)	8 (min # + 2)	10 (min # + 2 <sup>2</sup> )	12 (min # + 2 + 2 <sup>2</sup> )

FIGURE 5 (a)

Sel0	0	1	0	1	0	1	0	1
Sel1	0	0	1	1	0	0	1	1
Sel2	0	0	0	0	1	1	1	1
n (# of loads)	min + 0	min + 2	min + 4	min + 6	min + 8	min + 10	min + 12	min + 14

FIGURE 5 (b)

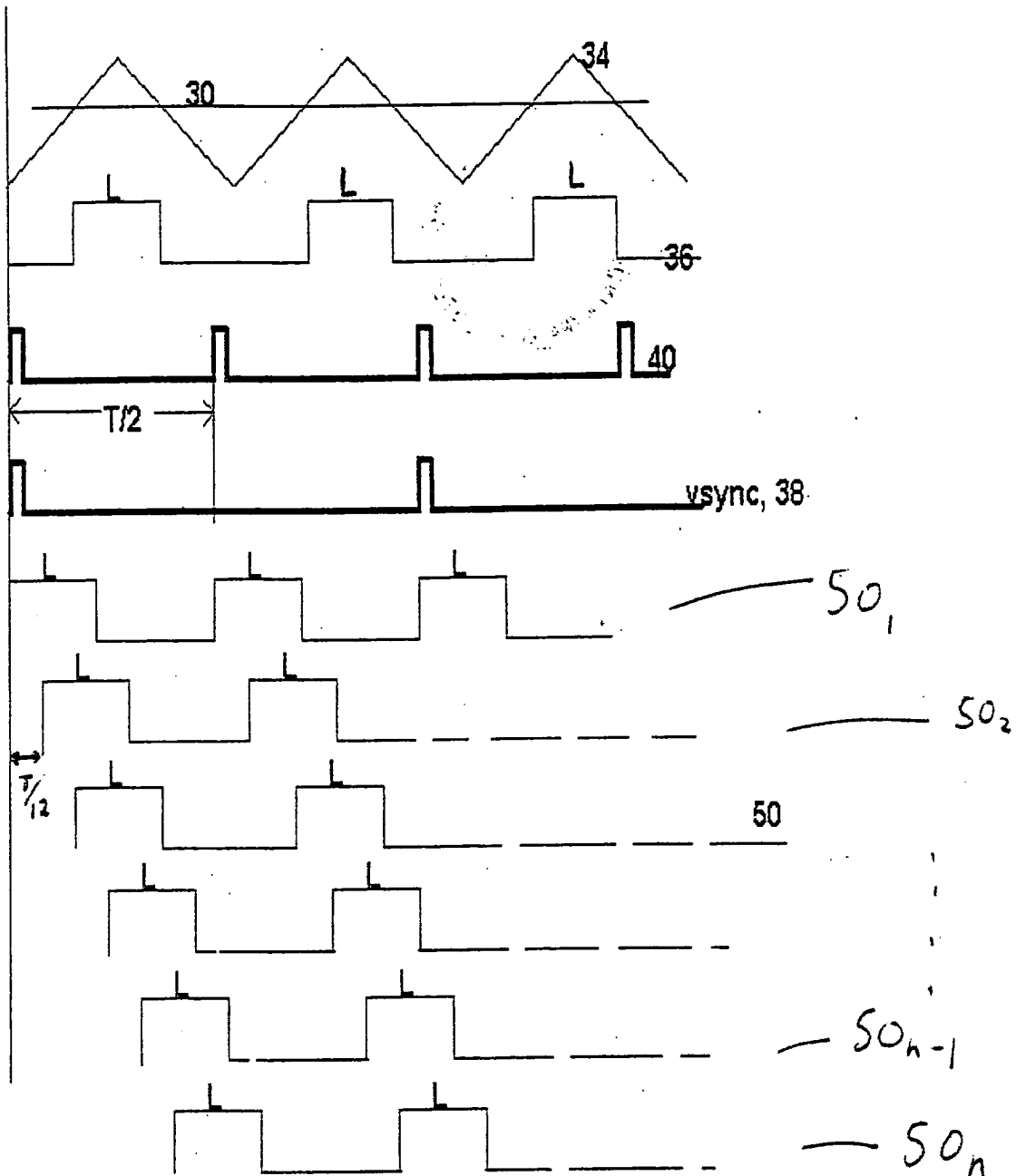


FIGURE 6

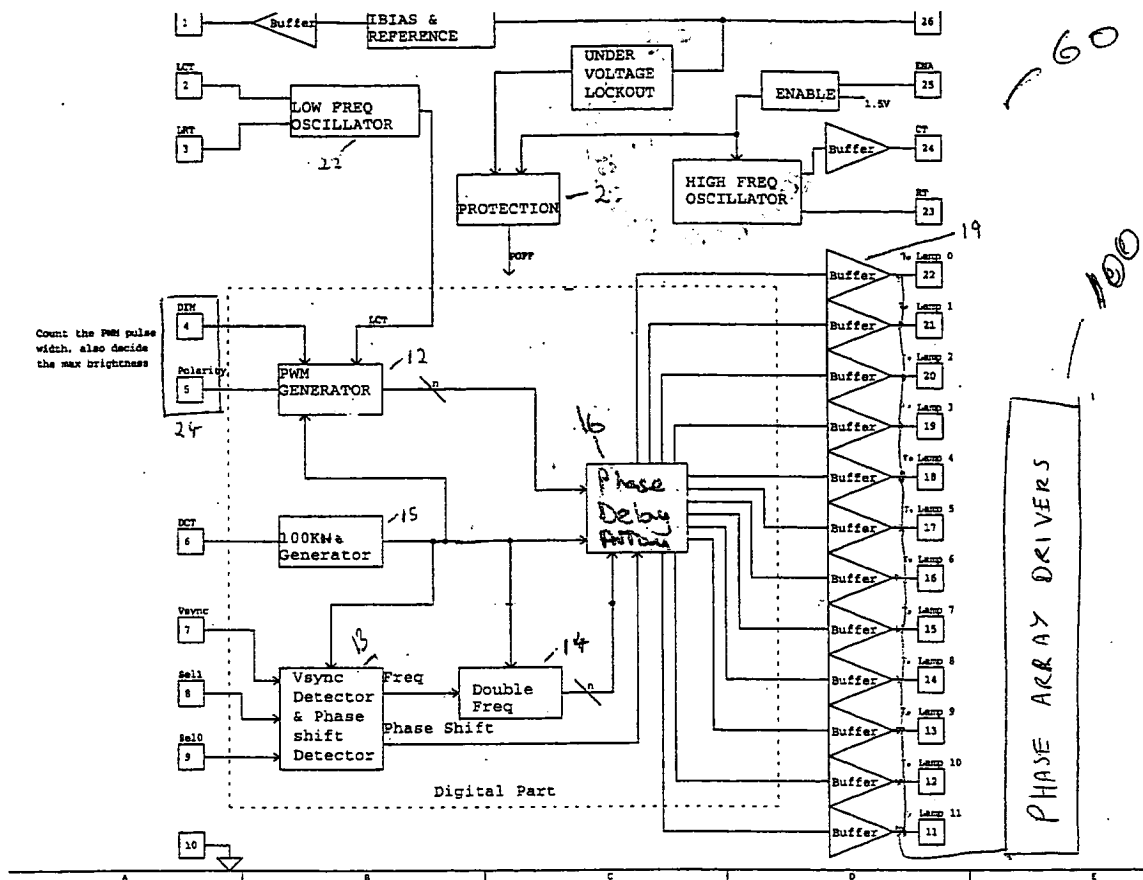


FIGURE 7

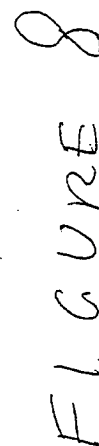


FIGURE 8



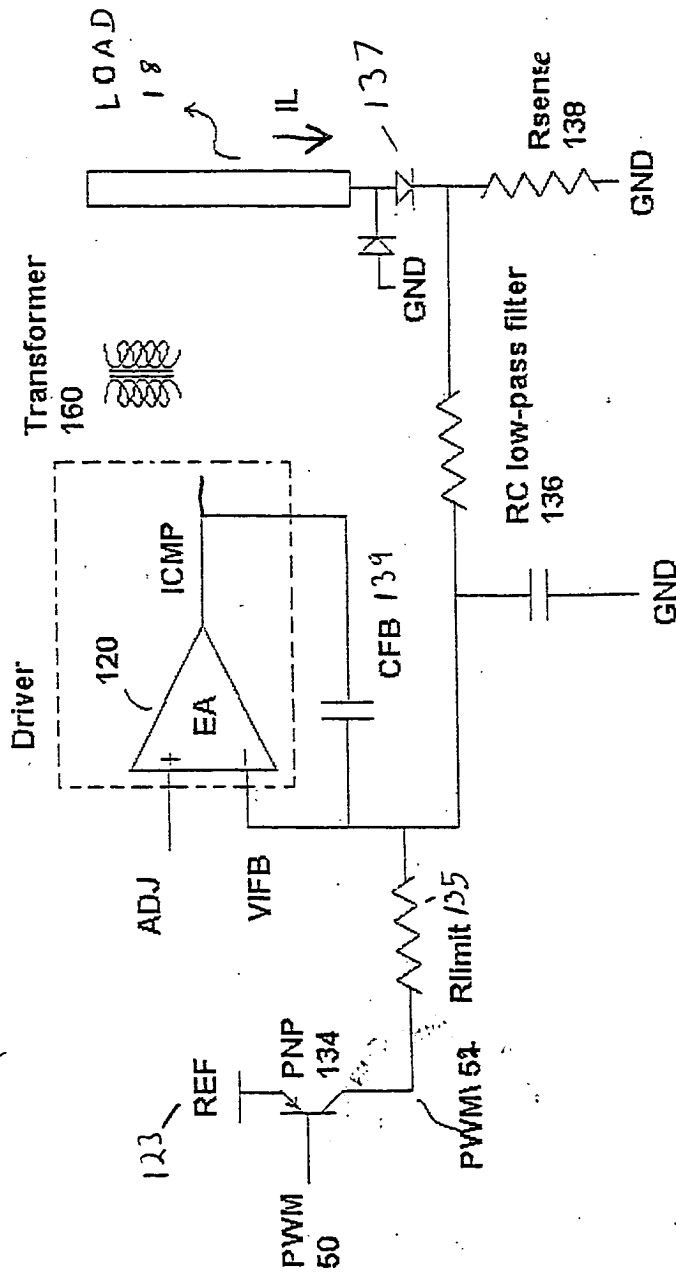


FIGURE 9

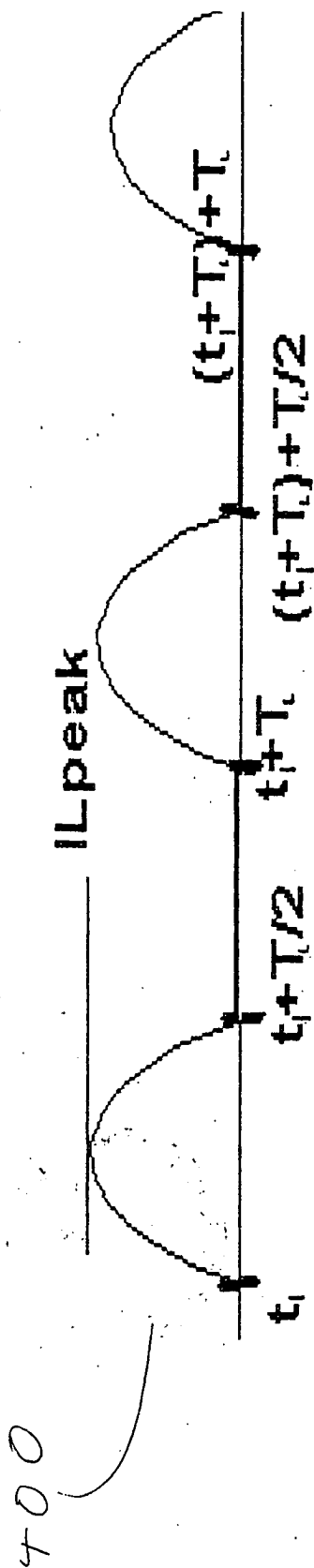


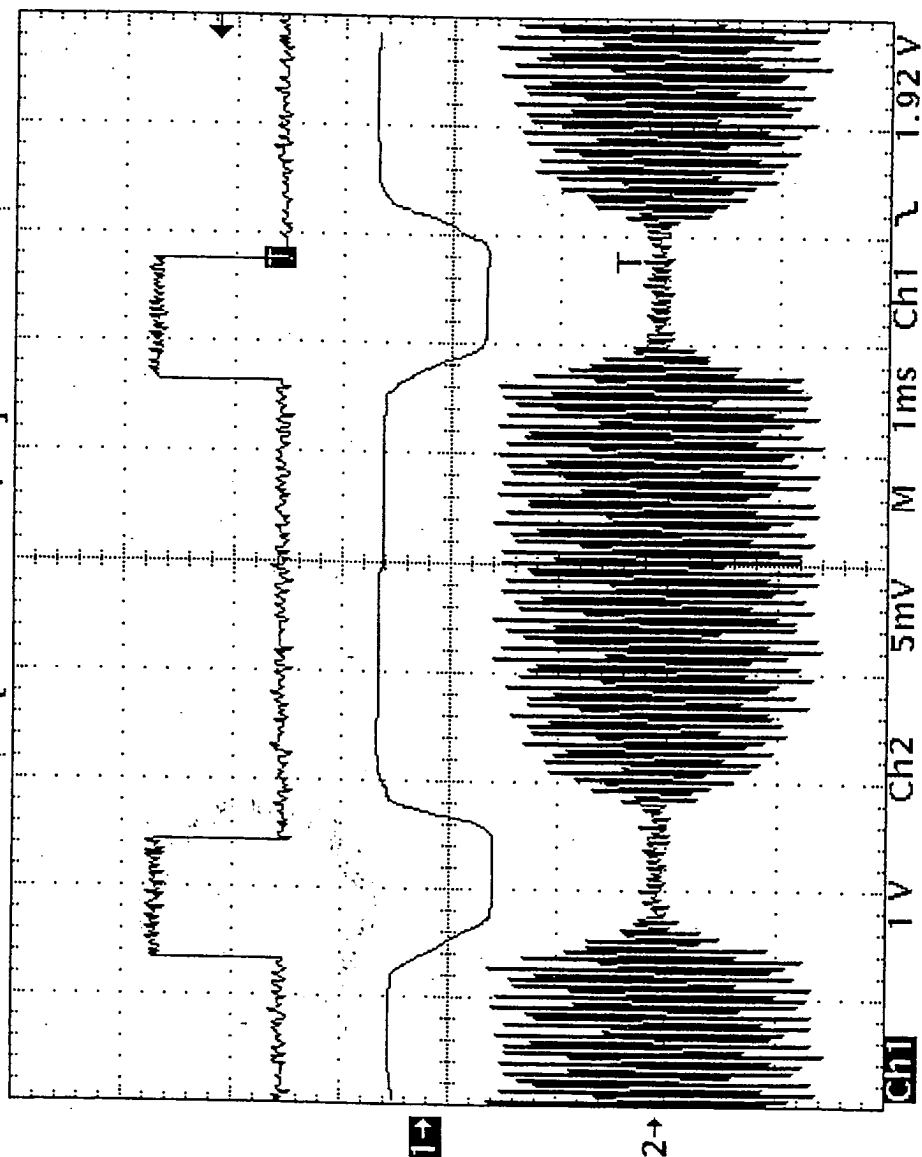
FIGURE 9a

Timing diagram for the burst mode of the 555 timer. The diagram shows four waveforms over time: 1CMP, PWM, PWM1, and FB. A vertical dashed line marks the 'SOFT START' point, after which the burst mode begins. The 1CMP signal is a square wave. The PWM signal is a square wave with a duty cycle that changes over time. The PWM1 signal is a square wave with a duty cycle that changes over time. The FB signal is a square wave that is high during the burst mode and low otherwise.

Figure 10

**Tek Stop: 50ks/s**

201 Acqs

$$\left[ \begin{array}{c} T \\ \vdots \\ T \end{array} \right]$$


52 ✓

**PWM:**

# ICMP

**Current**  
**[IL]**

11 May 2000  
11:44:20

FIGURE 11

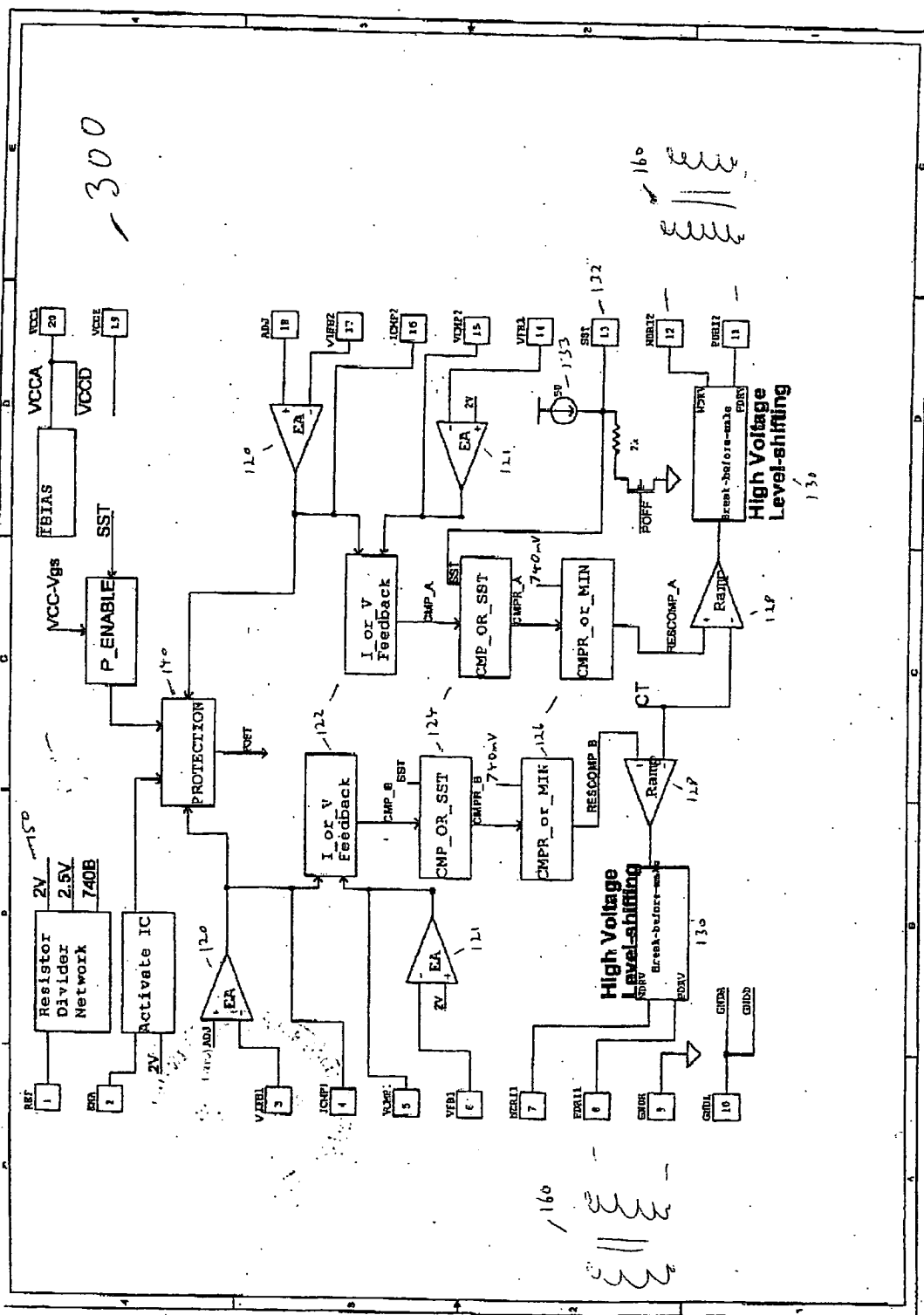


FIGURE 12

REF ID: A66260

350

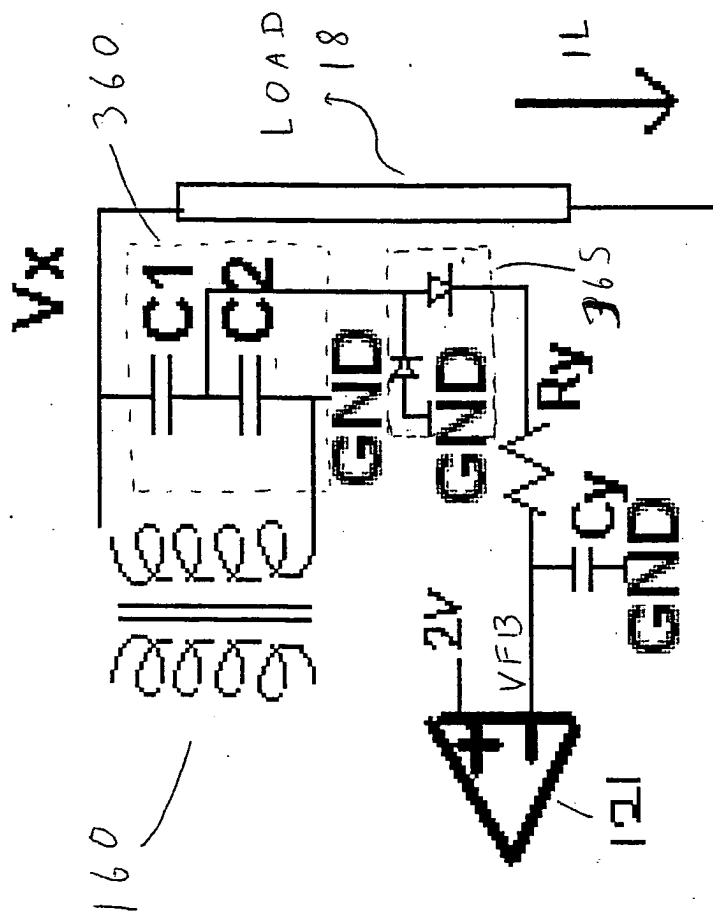


FIGURE 13

TOP SECRET

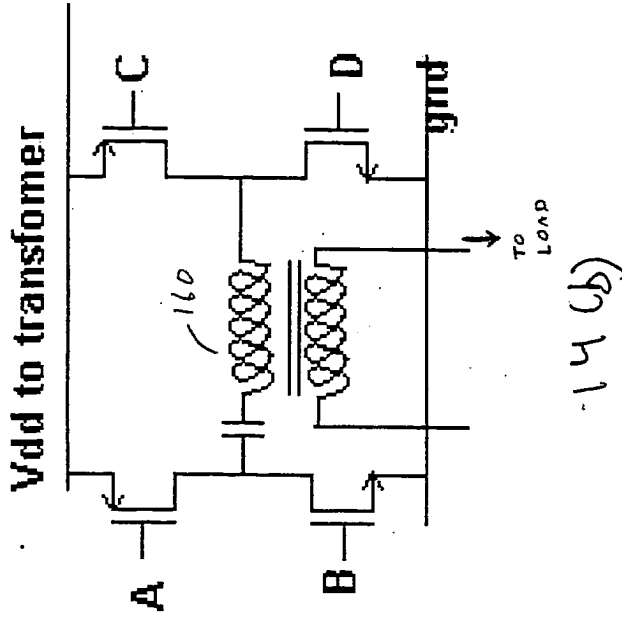
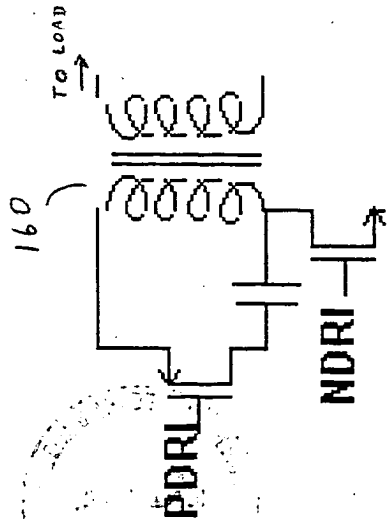


FIGURE 14

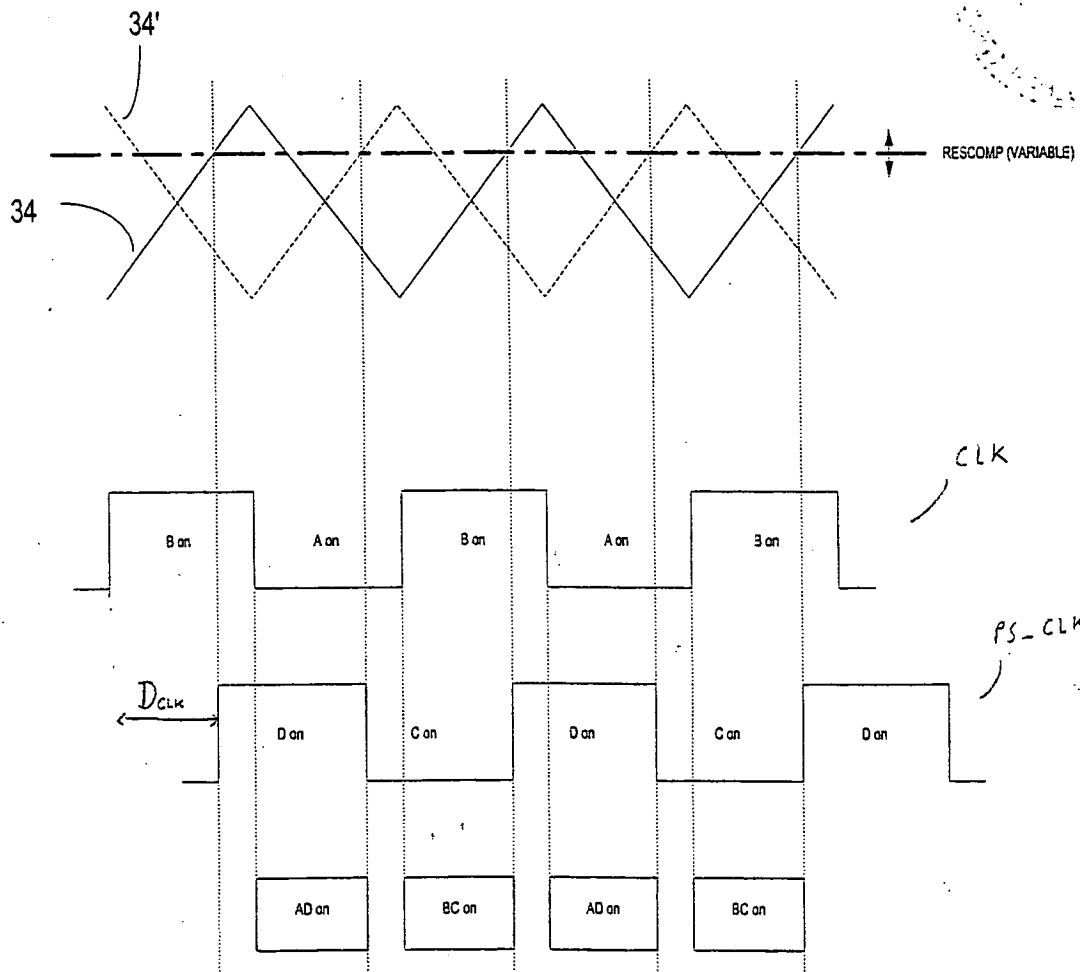


FIGURE 15